

A view of the object code

Translation of $(x+2)*(y-x)$

The external representation (concrete syntax) is a three statement sequence below:

```
t1 := x.loc + #2
t2 := y.loc - x.loc
t3 := t1 * t2
```

The internal representation of a 3AC statement is simply **one** (possibly 32 bits) **word** with the following internal structure:
quadruple

opt	source1	source2	result
-----	---------	---------	--------

Hence, the following is the store map of the code

:=/+	x.loc	2	t1
:=/-	y.loc	x.loc	t2
:=/*	t1	t2	t3

What about the Translation of Commands: Assignment

How does it to do it ?

$\frac{S \vdash e_l \rightarrow (S,l) \quad S \vdash e_r \rightarrow (S,v)}{S \vdash e_l := e_r \rightarrow S[v/l]}$	(Semantics of assignment for expressions without)
$\frac{l \vdash e_l \Rightarrow ([l l],l) \quad l \vdash e_r \Rightarrow ([l r],r)}{l \vdash e_l := e_r \Rightarrow [l l][l r][l l] := r}$	(Code Translation of assignments)

```
[11]C ::= A
[12]C ::= W
[13]A ::= ide := E {emit{ide.loc} := "E.loc}
```

In the command translation, we do not need any location in addition to the code that is anyway, generated by side effects

What about the Translation of Commands: Iterators

How does it to do it ?

$$\frac{S \vdash e \rightarrow (S, \text{true}) \quad S \vdash c \rightarrow S'}{S \vdash \text{while } e \text{ do } c \rightarrow S'} \quad \text{(Semantics of while)}$$
$$\frac{S \vdash e \rightarrow (S, \text{false})}{S \vdash \text{while } e \text{ do } c \rightarrow S}$$
$$\frac{\vdash e \Rightarrow ([le], I) \quad S \vdash c \Rightarrow [lc]}{\vdash \text{while } e \text{ do } c \Rightarrow ([le]ll\dots ll[lc]ll\dots)}$$

(Code Translation of while)

How do code [le] and code [lc] have to be combined together ?

What about the Translation of Combining Control Flows

[IEI]

if E.loc = #False goto nextC

[ICI]

[ICsI]

goto initC

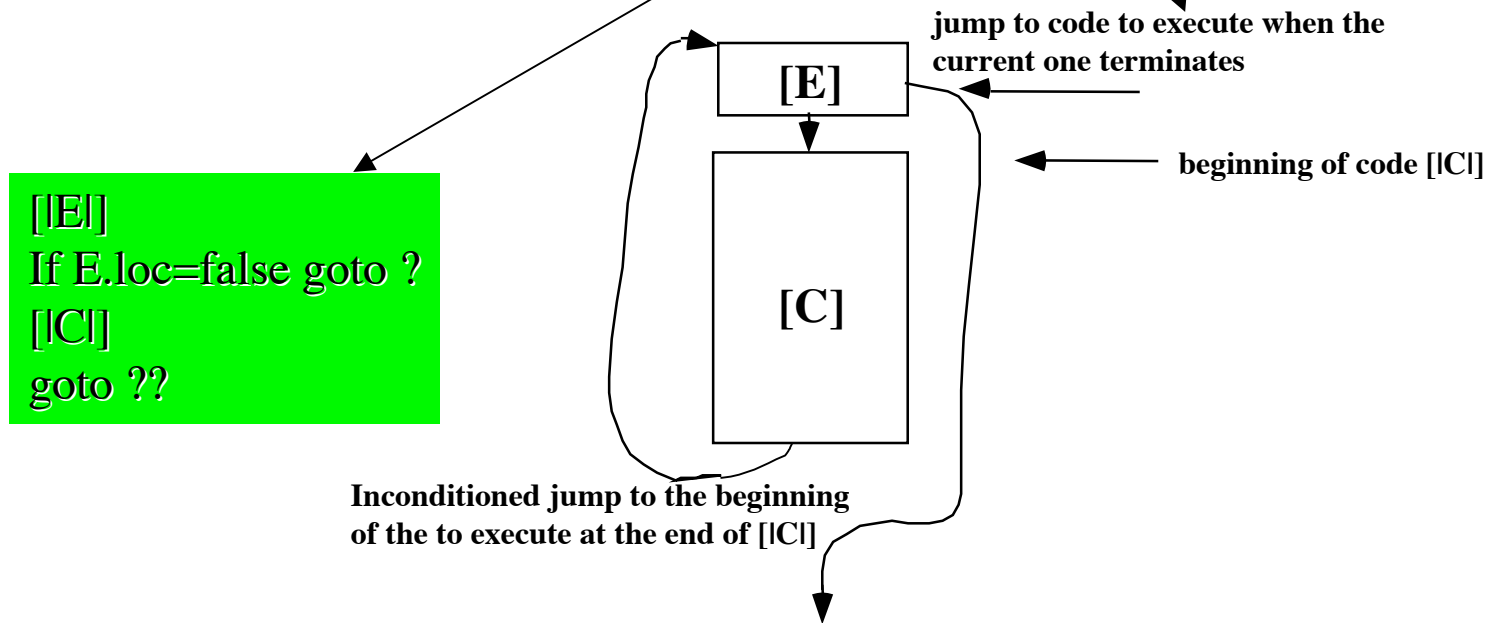
```
[14]W ::= while  
      E do {emit("if" E.loc "= #False goto" ?)}  
      C  
      Cs endw {emit("goto" ?)}
```

How do (instruction) locations nextC and initC have to be found and then, expressed in the translation actions of the grammar?

Commands that Change the Control Flow

Generation Code Schema

[10]W ::= while E do C endw



[10]W ::= while E do {emit('if' E.loc '=' 'false' goto --)} C {emit(goto --)} endw

Using a counter *quad* in order to have a map of the instruction positions in the program

quad

- is a counter of the compiler
- contains the index of the next line of the emit-file to be allocated
- initially, is set to 0
- is incremented by 1, after each execution of emit

```
[10]W ::= while {init:= quad} E do
           {emit('if' E.loc '=' 'false' goto --)} C
           Cs
           {emit(goto init)} endw
```

Using a *Backpatching* in order to handle with jumps to unpredictable positions ahead

Backpatching and operation *BK*

- is used for references to the position R of each instruction I not yet generated
- It consists in:
 - **collecting** into a list L(R) all the positions of the instructions J that need such a position in the target operand (e.g.: goto R)
 - **letting** unspecified (i.e. marked "-") the value R in each J when J is right generated
 - **updating** each J in the list L(R) with the value found for R when the I eventually generated
 - Instruction update is expressed with action: **BK(L(R),R)**

[10]W ::= while {init:= quad}

 E do {w.next:=mk-L(quad);
 emit('if' E.loc '=' 'false' goto --)}

 C

 Cs {emit(goto init)}

endw

What about the translation when C is,
in turn, a command while

Where $mk-L(a)=[a]$; $mk(a,L)=a::L$; $emptylist=[]$; $append=+$

Translation Invariant of Command the attribute *.next*

- Each command is possibly, a source of control transfer
- Hence, each command has an attribute *.next*
- *C.next* = target-uncompleted instructions of C, that have to be completed with the address of code, to execute when C terminates

```
[10]W ::= while {init:= quad}
      E do {w.next:= mk-L(quad);
            emit("if" E.loc "= #false goto --")}
      C {BK(C.next, quad)}
      Cs {BK(Cs.next, init); emit(goto init)}
      endw
```


Revisiting all the Phases of the One-Pass, Compositional, Code Translation

Use of side-effects: emit

```
[10]W ::= while E do {emit('if' E.loc '=' 'false' goto --)} C {emit(goto --)} endw
```

Use of offset for code locations: quad

```
[10]W ::= while {init:= quad} E do  
    {emit('if' E.loc '=' 'false' goto --)} C  
    {emit(goto init)} endw
```

Translation Invariant for Commands: .next

```
[10]W ::= while {init:= quad} E do  
    {w.next:=mk-L(quad);  
    emit('if' E.loc '=' 'false' goto --)} C  
    {emit(goto init)} endw
```

Use of Backpatching: BK(...)

```
[10]W ::= while {init:= quad} E do  
    {w.next:= mk-L(quad);  
    emit('if' E.loc '=' 'false' goto --)} C {BK(C.next, init);  
    emit(goto init)} endw
```

What about the Translation of Command Sequencing

How does it to do it ?

$$\frac{S \vdash c \rightarrow S' \quad S' \vdash cs \rightarrow S''}{S \vdash c \ cs \rightarrow S''} \quad \frac{}{S \vdash \lambda_c \rightarrow S} \quad \text{(Semantics of code sequencing)}$$

```
[5] Cs1 ::= ; C {BK(C.next, quad)}  
      Cs2 {Cs1.next:= Cs2.next}  
[6] Cs ::= ε {Cs.next:= []}  
...
```

Boolean Expression:

Short Circuit Translation for Control Transfer

Code Translation of a boolean expression B is, in short circuit, producing code that, when executed, transfers the control to two distinct code addresses:

- code to execute if B evaluates to true
- code to execute if B evaluates to false

Hence Short Circuit based Translation of expressions uses a different Translation Invariant:

- attribute *.true*: target-uncomplete for true case transfer
- attribute *.false*: target-uncomplete for false case transfer

source

$(x < y) \text{ or } z$

object

if x.loc < y.loc goto *true-point*
if z.loc goto *true-point*
goto *false-point*

**Currently unknown:
To be completed with
the effective addresses**

Short Circuit Translation for Control Transfer

Hence Short Circuit based Translation of expressions uses a different Translation Invariant:

- attribute *.true*: target-uncomplete for true case transfer
- attribute *.false*: target-uncomplete for false case transfer

$E_b ::= E_{b1} \text{ or } M E_{b2}$

**$E_b.true ::= \text{app}(E_{b1}.true, E_{b2}.true),$
 **$E_b.false ::= E_{b2}.false,$
 $BK(E_{b1}.false, M.quad)$****

$E_b ::= E_{b1} \text{ and } M E_{b2}$

**$E_b.true ::= E_{b2}.true,$
 **$E_b.false ::= \text{app}(E_{b1}.false, E_{b2}.false),$
 $BK(E_{b1}.true, M.quad)$****

Where $mk-L(a)=[a]$; $mk(a,L)=a::L$; $emptylist=[]$; $append=+$

Short Circuit Translation for Control Transfer / 2

Eb::= true

```
Eb.true::= MK(quad, emptylist),  
Eb.false::= emptylist,  
emit('goto' _)
```

Eb::= false

```
Eb.true::= emptylist,  
Eb.false::= MK(quad, emptylist),  
emit('goto' _)
```

Eb::= ide

```
Eb.true::= MK(quad, emptylist),  
Eb.false::= MK(quad+1, emptylist),  
emit('if' ide.loc '=' #true goto' _),  
emit('goto' _)
```

Where $mk-L(a)=[a]$; $mk(a,L)=a::L$; $emptylist=[]$